PATENT

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Applicant(s): David L. Reese, et al.

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Title: PROFILING OF COMPUTER PROGRAMS EXECUTING IN VIRTUAL MEMORY SYSTEMS

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PRELIMINARY AMENDMENT AND TRANSMITTAL OF FORMAL DRAWINGS

Kindly amend the application as follows.

In the Specification:

Please amend the specification as follows.

At page 5, line 2, change "schedules" to -- scheduled --.

At page 5, line 5, change "a one" to --one--.

At page 8, line 2, change "which at which at" to --which at--.

At page 9, lines 28-29, change "for translating address references are" to --translates address references--.

At page 10, line 4, change "Preferred embodiments" to --Embodiments--.

At page 11, line 23, change "Preferred embodiments" to --Embodiments--.

At page 12, line 16, add a period at the end of the line.

30585/6: At page 13, lines 30-31, change "binary translator 124" to --the binary translator--.

At page 14, line 1, change "Preferred embodiments" to --Embodiments--.

30585/6: At page 15, lines 15, change "binary translator 124" to --the binary translator--.

At page 20, lines 9-10, change "the I-cache" to --I-cache 112--.

At page 25, lines 7-12, replace the sentence "Translator 124 ... data)." with the following: --Translator 124 also assumes that all memory references are to well-behaved memory. ("Well-behaved memory" is a memory from which a load will receive the data last stored at the memory location. Non-well -behaved memory is typified by memory-mapped